Super Attenuator Control System

Activities Report

Cascina , May 20th 2014

Alberto Gennai (INFN Pisa)

Team

- D. Passuello (INFN Pisa)
- A. Gennai (INFN Pisa)
- C. Magazzu (INFN Pisa) – HW des. & dev.
- V. Boschi (University of Pisa) - Controls
- M. Bitossi (EGO) – HW & FW des. & dev.
- C. Carissimi (EGO – since April 28th) – HW des. & dev.
- A. Kutynia (Poland, till next June) – HW des. & dev.
- L. Rei (INFN Genova) – SW
- EGO Support for SW and HW

Status Report 1/2

- Hardware
  - A second version of electronic hardware was developed and went through an internal review last week.
  - Components were added to a database that will help during production
- Firmware
  - We received the TOLM firmware from LAPP and it was integrated in our design. We succeeded in compiling the whole design and functional test will follow soon

Status Report 2/2

- Software
  - Work is proceeding well on DSP compiler and server side
  - We are still in a preliminary phase for what concerns supervisor
- Orders
  - Preparatory work for the crates call for tender (~160 kEuro + VAT)
  - Passive components procurement will be assigned to firm in charge of assembly

Next Steps

- Call for tender for crates
- Place orders for chips having long shipment time
- Test TOLM interface (in collaboration with LAPP)
- Produce second version of hardware
- As soon as BS pinout of in vacuum cabling will be available, distribution frame design will start. In the meanwhile we are starting NE distribution frame design since pinout here is already available.